

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S64	397	adkisson.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/20 10:10
S65	102	S64 and richard	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/20 10:10
S66	11	S65 and ((counter and compare) with circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/20 10:11
S67	92475	johnson.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/20 10:12
S68	247	S67 and tyler	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/20 10:12
S69	6	S68 and ((counter and compare) with circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/20 10:13
S70	62237	(hewlett-packard. as.) or ((hewlett adj packard.as.))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/20 10:14
S71	72	S70 and ((counter and compare) with circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/20 10:15

S72	0	702/186.ccls. and ((counter and compare) with circuit) and mode and increment\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/20 10:21
S73	115	"702"/\$.ccls. and ((counter and compare) with circuit) and mode and increment\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/20 10:21
S74	11	714/47.ccls. and ((counter and compare) with circuit) and mode and increment\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/20 10:48
S75	7	714/48.ccls. and ((counter and compare) with circuit) and mode and increment\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/20 10:49
S76	6	714/49.ccls. and ((counter and compare) with circuit) and mode and increment\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/20 10:49
S77	1	714/1.ccls. and ((counter and compare) with circuit) and mode and increment\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/20 10:50
S78	1	714/2.ccls. and ((counter and compare) with circuit) and mode and increment\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/20 10:50
S79	4	714/3.ccls. and ((counter and compare) with circuit) and mode and increment\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/20 10:50
S80	15	714/25.ccls. and ((counter and compare) with circuit) and mode and increment\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/20 10:50

S81	0	714/669.ccls. and ((counter and compare) with circuit) and mode and increment\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/20 10:50
S82	16	714/704.ccls. and ((counter and compare) with circuit) and mode and increment\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/20 10:50
S83	4	714/706.ccls. and ((counter and compare) with circuit) and mode and increment\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/20 10:51
S84	2	714/709.ccls. and ((counter and compare) with circuit) and mode and increment\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/20 10:51
S85	35	714/724.ccls. and ((counter and compare) with circuit) and mode and increment\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/20 10:52
S86	5	714/735.ccls. and ((counter and compare) with circuit) and mode and increment\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/20 10:52
S87	22	714/736.ccls. and ((counter and compare) with circuit) and mode and increment\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/20 10:53
S88	538	"714"/\$.ccls. and ((counter and compare) with circuit) and mode and increment\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/20 10:56
S89	69	S88 and match\$3 and threshold	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/20 10:56

S90	6	712/224.ccls. and ((counter and compare) with circuit) and mode and increment\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/20 11:00
S91	7	716/4.ccls. and ((counter and compare) with circuit) and mode and increment\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/20 11:00
S92	795	((counter and compare) with circuit) and bus and match\$3 and threshold and mode and increment\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/20 11:01
S94	79	S92 and debug and event and signal	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/20 11:05
S95	1	((((counter and compare) with circuit) and bus and match\$3 and threshold and mode and increment\$3 and debug and event and signal).CLM.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/20 11:06
S96	10093	performance with counter	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/20 12:15
S97	369	S96 and ((counter and compare) with circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/05/20 12:16

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